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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Charles P. Roth et al. Art Unit: 2183
Serial No.: 09/704,467 Examiner: Aimee J. Li
Filed : October 31, 2000 Assignee: Intel Corporation

Title : EFFICIENT EMULATION INSTRUCTION DISPATCH BASED ON
INSTRUCTION WIDTH

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

AMENDMENT

In response to the Office Action mailed September 9, 2003,
please amend the application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 6 of this paper.

Remarks begin on page 14 of this paper.

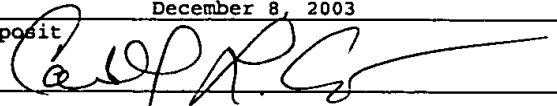
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